



SREE VAHINI INSTITUTE OF SCIENCE & TECHNOLOGY
Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

Department of Electrical & Electronics Engineering

A) Profile of each faculty give one page covering with passport size photograph

- | | | |
|---------------------------------|---|--|
| 1. Name of the faculty | : | SRINIVASA RAO TADANKI |
| 2. Date of Birth | : | 10/06/1978 |
| 3. Unique ID | : | |
| 4. Education qualifications | : | B.Tech, M.Tech(Ph.D) |
| 5. Work experience | : | 10 Years |
| 5.1. Teaching | : | 10 Years |
| 5.2. Research | : | -- |
| 5.3. Industry | : | -- |
| 5.4. Others | : | -- |
| 6. Area of Specialization | : | POWER SYSTEM ENGINEERING, MICRO GRIDS |
| 7. Courses taught at | : | |
| 7.1. Diploma | : | ACM-I, II, DCM, ED, EM, EC, DC LAB, AC LAB |
| 7.2. Post Diploma | : | -- |
| 7.3. Under Graduate | : | EM-I, II, PS-I, II, SGP, ET, NT, PSOC, EMS, PE, CS, EM-I LAB, EM LAB, EM-II LAB, EC LAB, PE LAB, CS LAB |
| 7.4. Post Graduate | : | SMART GRID, PSOC, MP&MC, RTCPS, PST |
| 7.5. Post Graduate Diploma | : | |
| 8. Research guidance | : | |
| 8.1. No. of papers published in | : | |
| a) National | : | |
| (Journals / Conferences) | : | |
| b) International | : | |
| (Journals / Conferences) | : | 03 |
| 8.2. Master | : | 02 |





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8.3. Ph. D	:	01
9. Projects carried out	:	25
10. Patents	:	--
11. Technology Transfer	:	--
12. Research Publications	:	--
13. No. of books published with details	:	--
14. No. of Workshops conducted / attended	:	03